

IP-XACT library in Kactus2

- VLVN= Vendor-Library-Name-Version
- Tools like Kactus2 explore the disk, find ip-xact xml-files and build a library model for the user
- IP-XACT objects are only referenced by VLVNs, not directory paths or file names
- Design hierarchy does not depend on file directory names, but we recommend that folders on disk are named according to VLVN
- Kactus2 adds attributes to IP-XACT objects for physical product hierarchy and firmness (vendor extensions to ip-xact)

IP-XACT Library

Item Type

Component Bus/API/COM Advanced

Implementation

HW SW System

Product Hierarchy

Flat Product Board
 Chip SoC IP

Firmness

Template Mutable Fixed

Library Filters

Vendor: TUT.fi
Library: soc
Name:
Version:

VLNV Tree Hierarchy

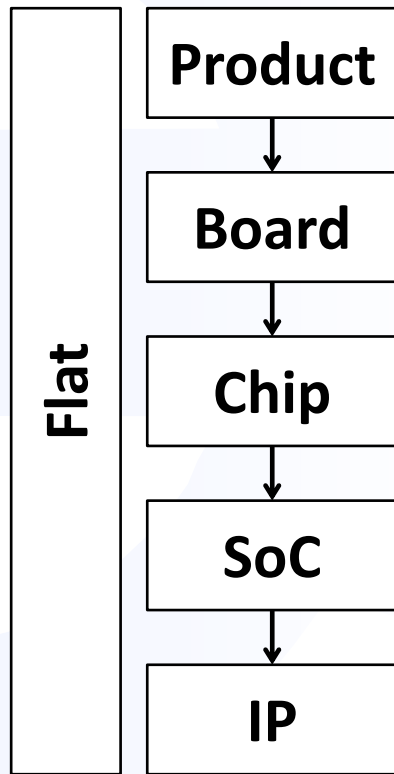
Library items

- ▲ TUT.fi
 - ▲ soc
 - ▲ hwp.memory.test.top
 - param_propagation

Product hierarchy attributes

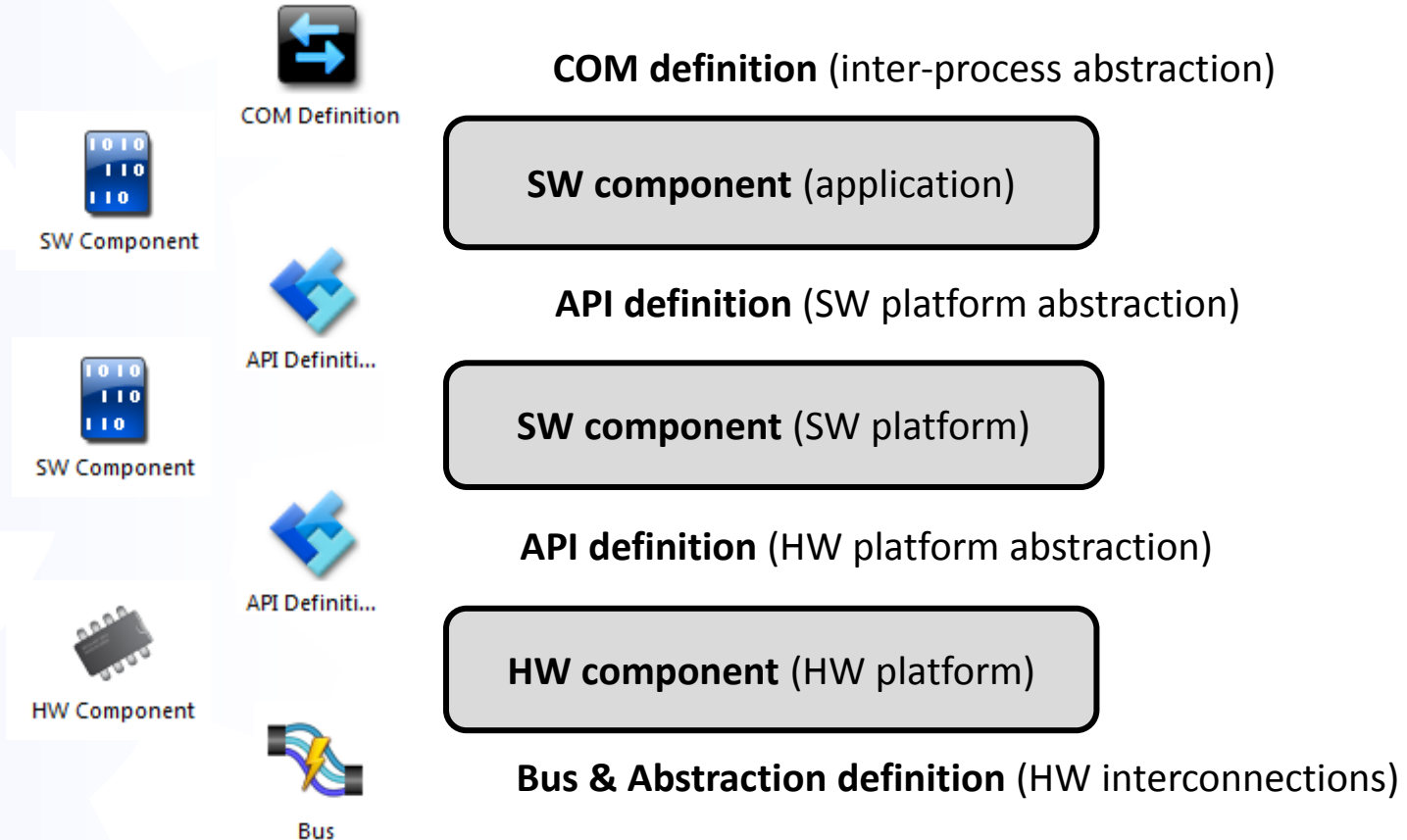
Vendor Library Name Version

Kactus2 VLibraryNV naming recommendation is based on product hierarchy

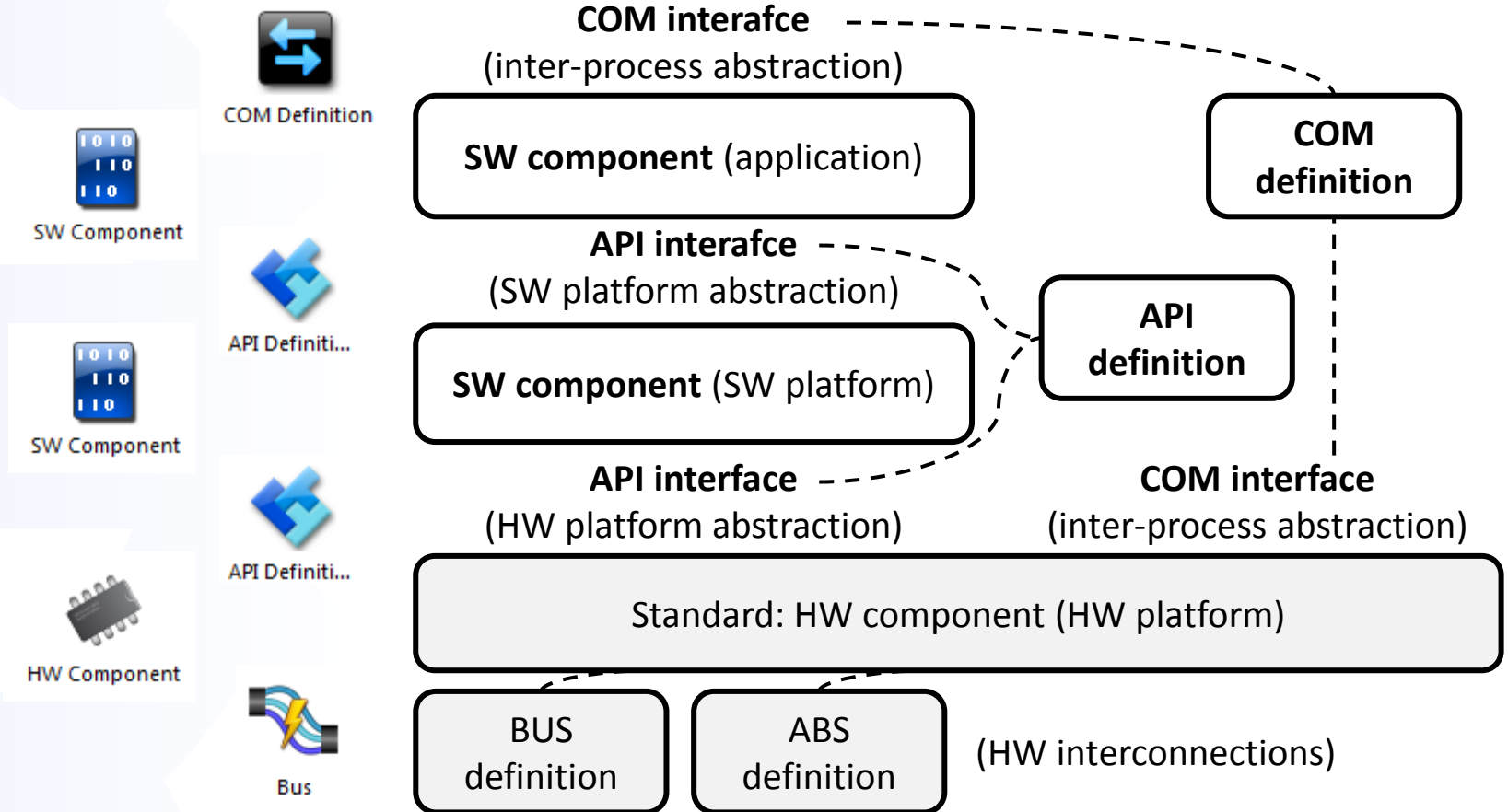


Vendor	Library	Name	Version		
Domain	Product hierarchy level / category	Function	draft or <string>		
<i>tut.fi</i>	<i>flat</i>	testsuite	draft		
	<i>product</i>	myproductX	1.0		
	<i>board</i>	fpgaboard	2020.1.2		
	<i>chip</i>	sdram8mb	passed.1		
	<i>soc</i>	aes			
	<i>ip</i>	<i>app</i>	hevcenc		
			<i>hwp</i>	<i>accelerator</i>	dct
				<i>communication</i>	wishbone
				<i>cpu</i>	riscv
			<i>interface</i>	pcie	
		<i>storage</i>	ddr2_ctrl		
		<i>support</i>	clock		
		<i>swp</i>	<i>api</i>	endpoint_4s	
			<i>driver</i>	usb	
<i>os</i>			linux_x.x.x		
<i>stack</i>	mcapi				

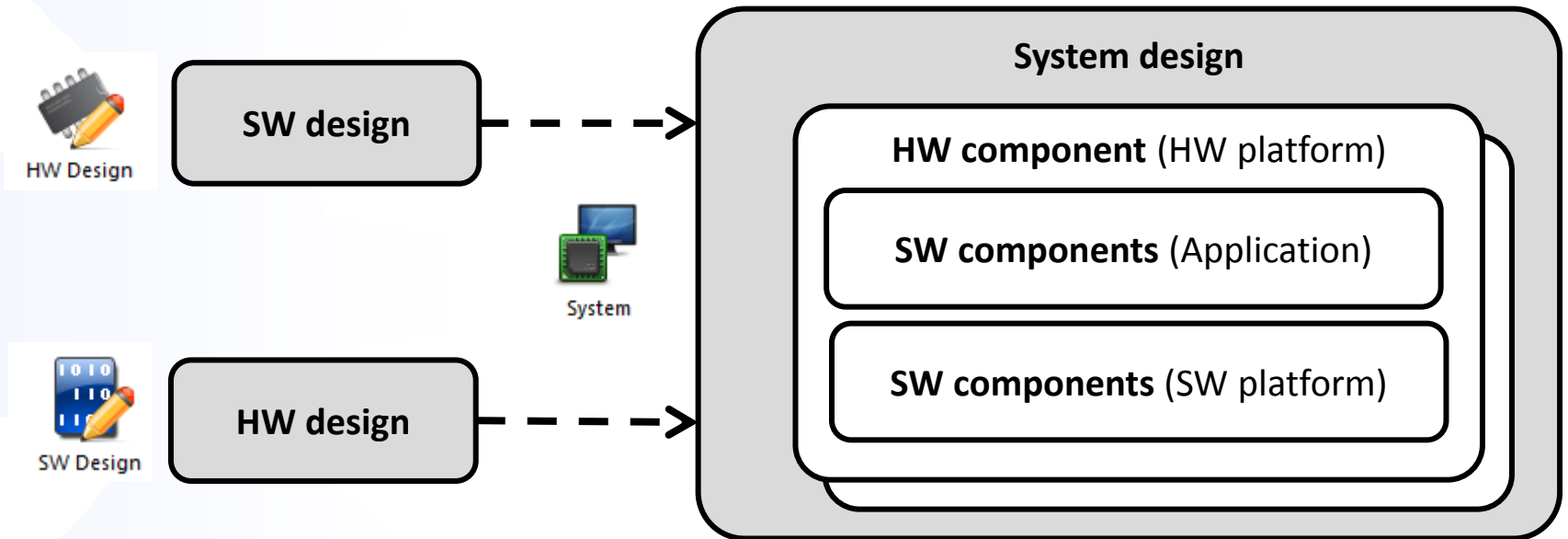
Kactus2 library item types follow software stack view to the product



Library item relations



Kactus2 system designs include flattened HW designs and SW components mapped



Example Product

Product

vndr_A FPGA board

PC

vndr_B FPGA board/chips



tut.fi – product – myproduct – 1.0

Product

vndr_A FPGA board

tut.fi – board – masterboard – 1.0

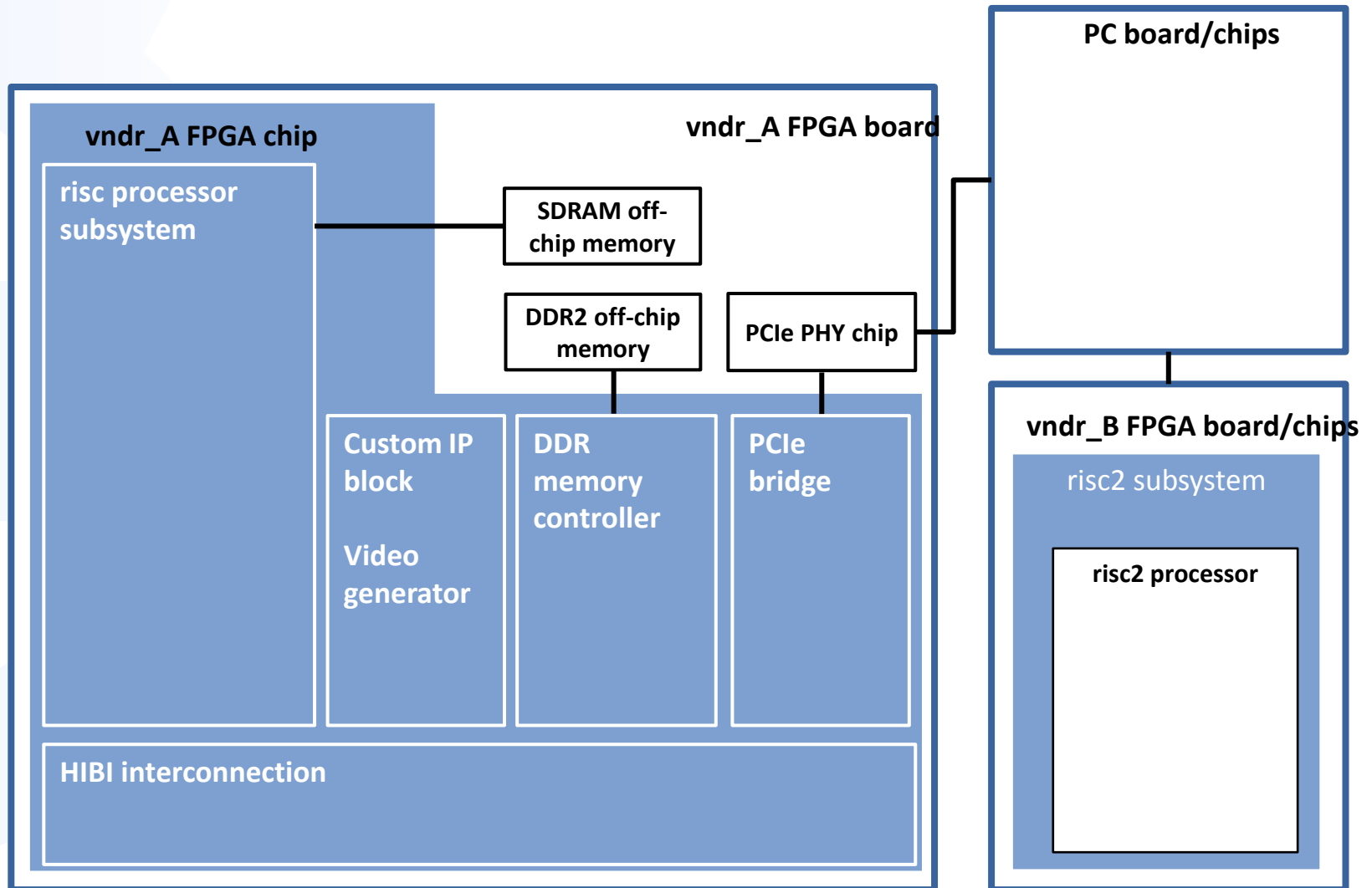
PC

tut.fi – board – std_pc – 1.0

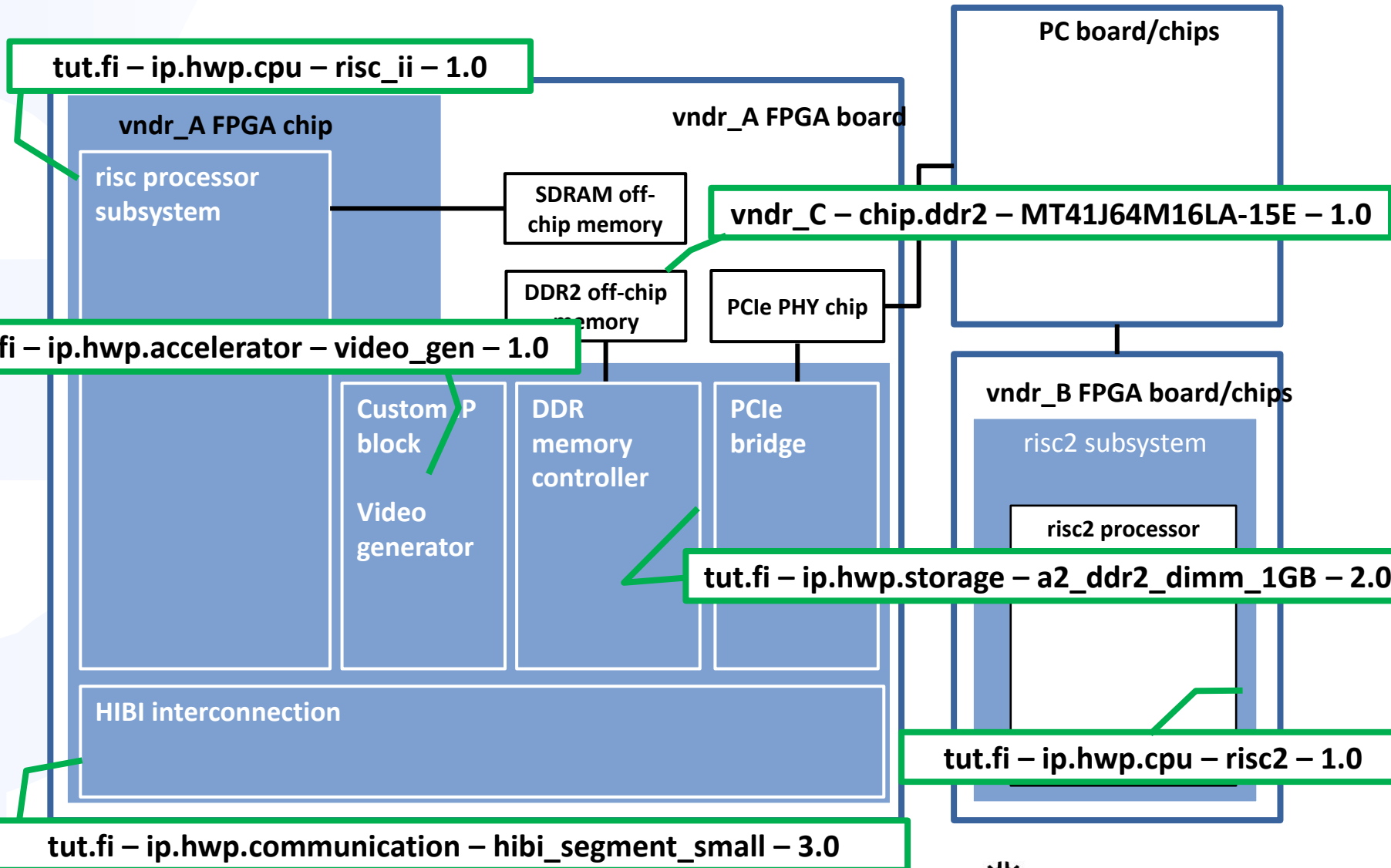
vndr_B FPGA board/chips

tut.fi – board – slaveboard – 1.0

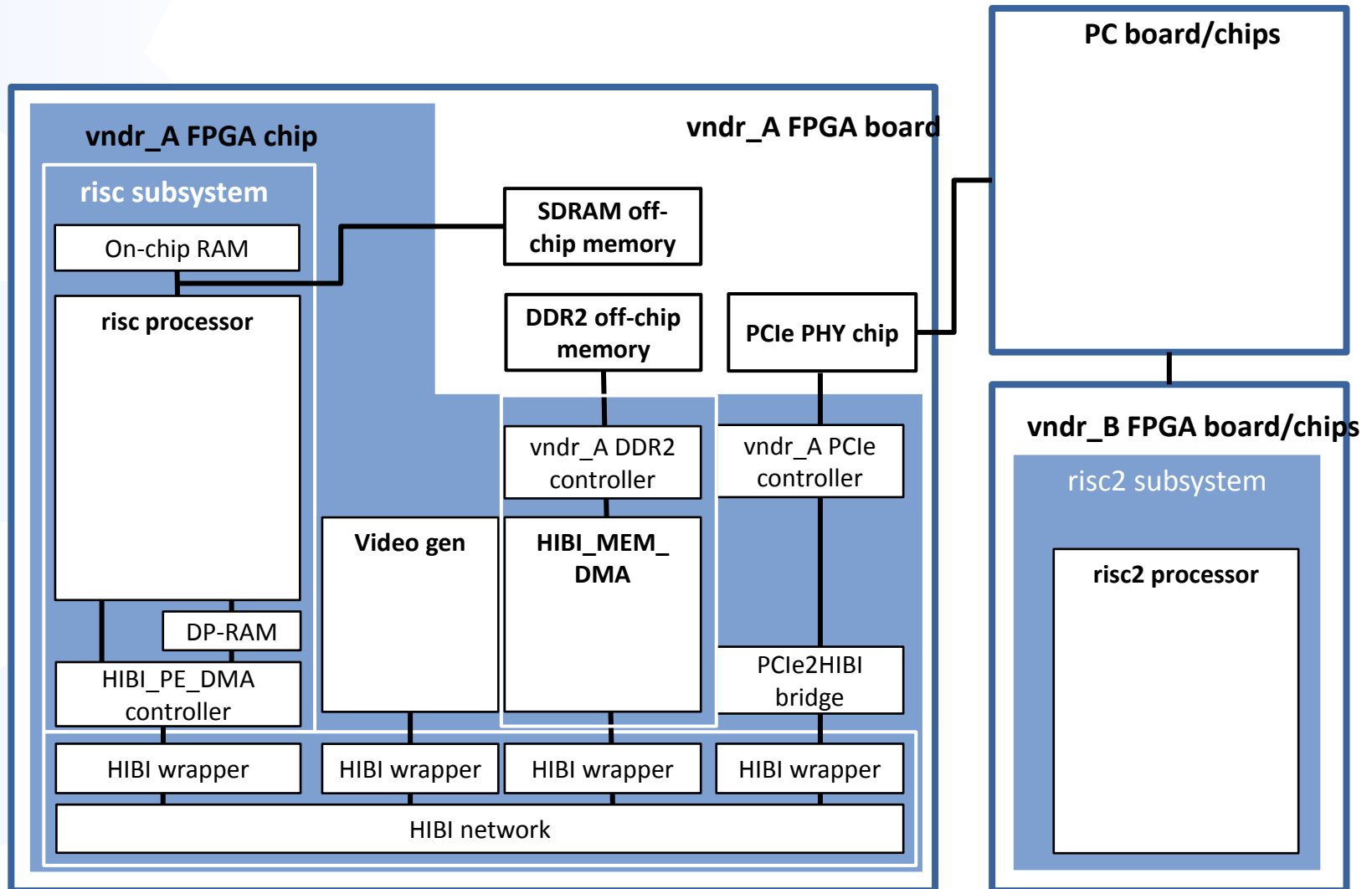
HW platform



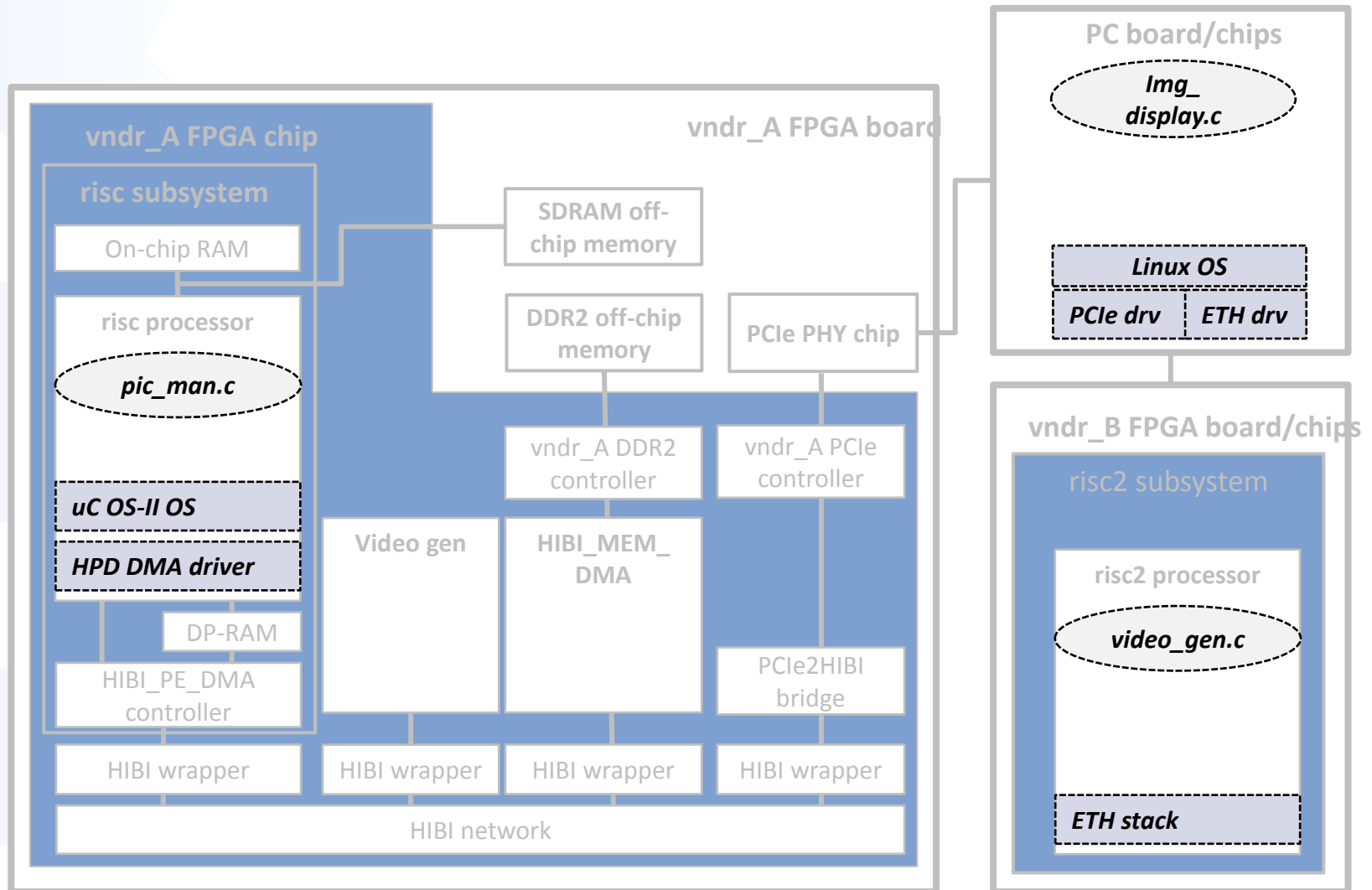
VLNV



HW platform in detail



SW platform, Application SW



VLNV

tut.fi – ip.application – img_display – 1.0

tut.fi – ip.application – pic_man – 1.0

tut.fi – ip.swp.stack – linux – 1.0

tut.fi – ip.swp.os – uc0sII – 1.0

tut.fi – ip.swp.driver – hibi_pe_dma – 1.0

tut.fi – ip.swp.stack – risc_hibi_uc0s – 1.0

